

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	Examiner: Not yet assigned
Suman Datta et al.)	
)	Art Unit: Not yet assigned
Serial No.: Not yet assigned)	
)	
Filed: Herewith)	
)	
For: A METHOD AND APPARATUS FOR)	
IMPROVING STABILITY OF A 6T CMOS)	
<u>SRAM CELL</u>)	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the documents cited on that form. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

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☐ A statement pursuant to 37 C.F.R. §1.97(e) or

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- (1) A statement pursuant to 37 C.F.R. §1.97(e); and
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 10/02, 2003



Michael A. Bernadicou
Reg. No. 35, 934

12400 Wilshire Blvd.
Seventh Floor
Los Angeles, CA 90025-1026
(408) 720-8300

Substitut for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	Not yet assigned
				Filing Date	Herewith
				First Named Inventor:	Suman Datta et al.
				Art Unit	Not yet assigned
Examiner Name	Not yet assigned				
She t	1	of	2	Attorney Docket Number	42P16682

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
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				Examiner Name	Not yet assigned
Sheet	2	of	2	Attorney Docket Number	42P16682
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
		Evert Seevinck, "Static-Noise Margin Analysis of MOS SRAM Cells", IEEE Journal of Solid State Circuits, Vol. Sc-22, No. 5, October 1987, pp 748-754			
		Peter A. Stolk et al., "Modeling Statistical Dopant Fluctuations in MOS Transistors", IEEE Transactions on Electron Devices, Vol. 45, No. 9, September 1998, pp. 1960-1971			
		Robert S. Chau et al., "Tri-Gate Devices and Methods of Fabrication", Application Serial Number 10/227,068, Filed August 23, 2002			
		Robert S. Chau et al., "Tri-Gate Devices and Methods of Fabrication", Application Serial Number 10/367,263, Filed February 14, 2003			

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